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# Topographie des spezifischen Widerstands semi-isolierender SiC-Substrate

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SiCrystal

# Übersicht

- Motivation
- **CO**ntactless **RE**sistivity **MA**pping (COREMA)
- Bewertung von lokal inhomogenem Material
- Widerstandstopogramme von SiC Substraten
- Zusammenfassung

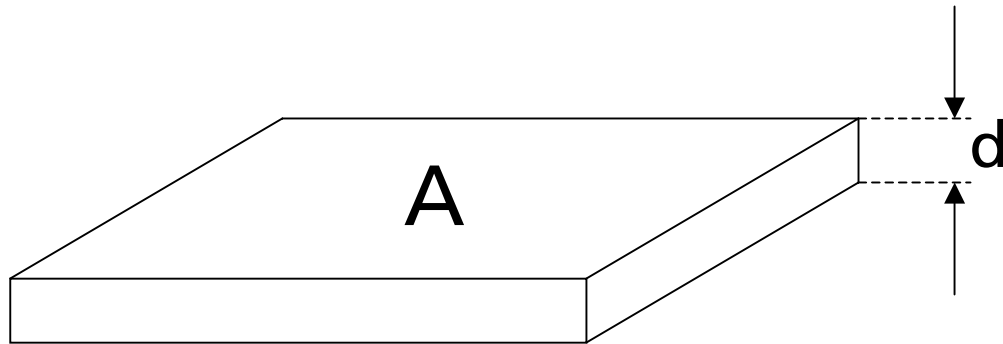
# Motivation

- Weltweit wird an der Entwicklung von (SI) SiC Wafern mit Durchmesser bis zu 100 mm gearbeitet
- Kontaktfreie, orts aufgelöste Widerstandsmessung an SiC Substraten ist aus mehreren Gründen erwünscht
- **C**ontactless **R**esistivity **M**apping (COREMA) wird industriell zur Charakterisierung von SI GaAs und InP Substraten eingesetzt
- Weiterentwicklung des **COREMA** - Systems für die Bewertung von SiC Substraten erscheint aussichtsreich

# Neue Anforderungen

- Erweiterung des Messbereiches auf  $1 \times 10^5 - 1 \times 10^{12} \Omega \text{cm}$
- Automatisierte Messung von lateral inhomogenem Material
- Bewertung von *lokal* inhomogenem Material

# Capacitive Resistivity Evaluation (I)



$$R_s = \rho d/A$$

$$C_s = \varepsilon A/d$$

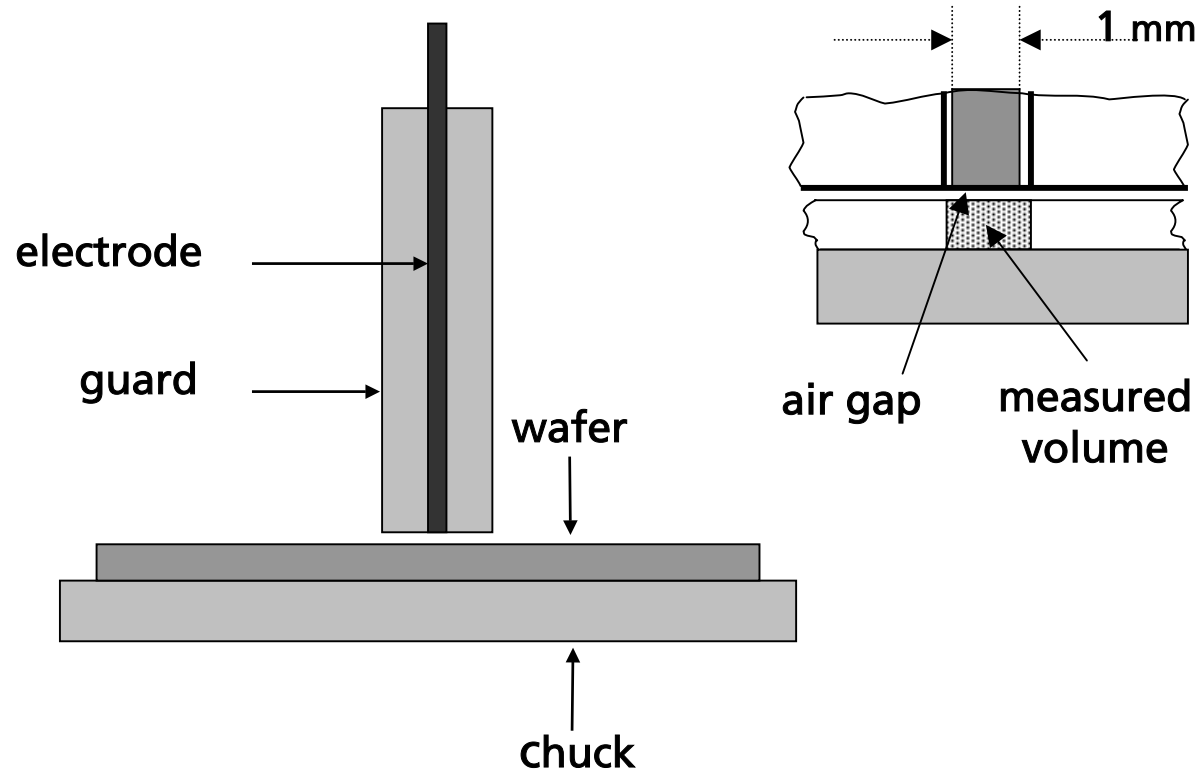
$$R_s C_s = \rho \varepsilon = \tau$$

**Semi-insulating semiconductor**

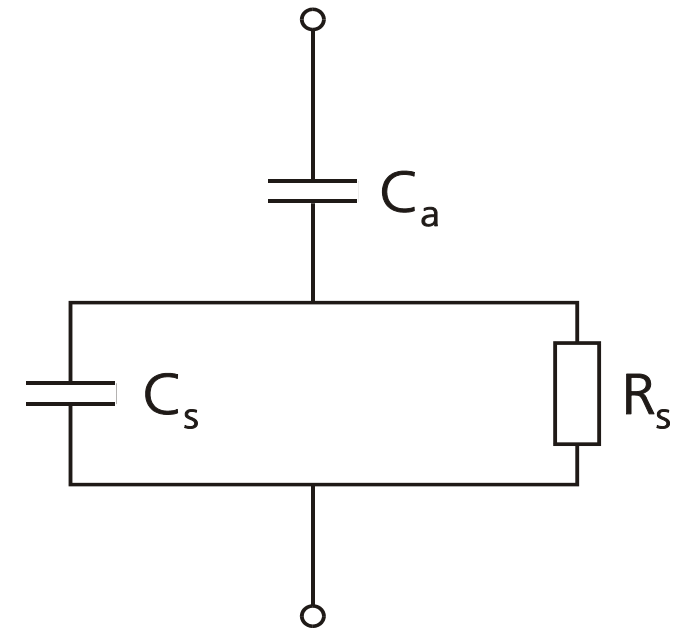
$$\rho = \tau / \varepsilon$$

# Capacitive Resistivity Evaluation (II)

## Capacitive Probe



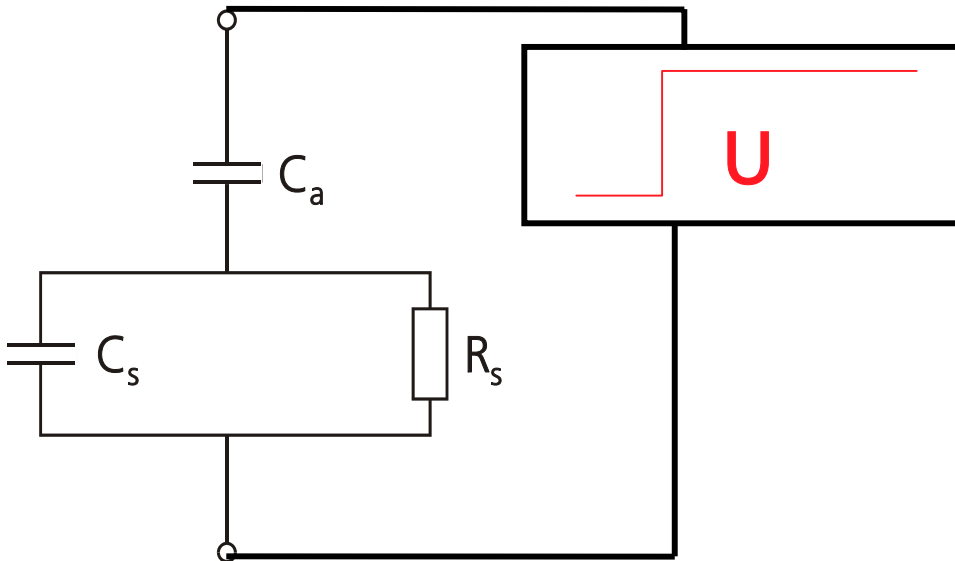
## Equivalent Circuit



$$\tau = R_s (C_s + C_a)$$

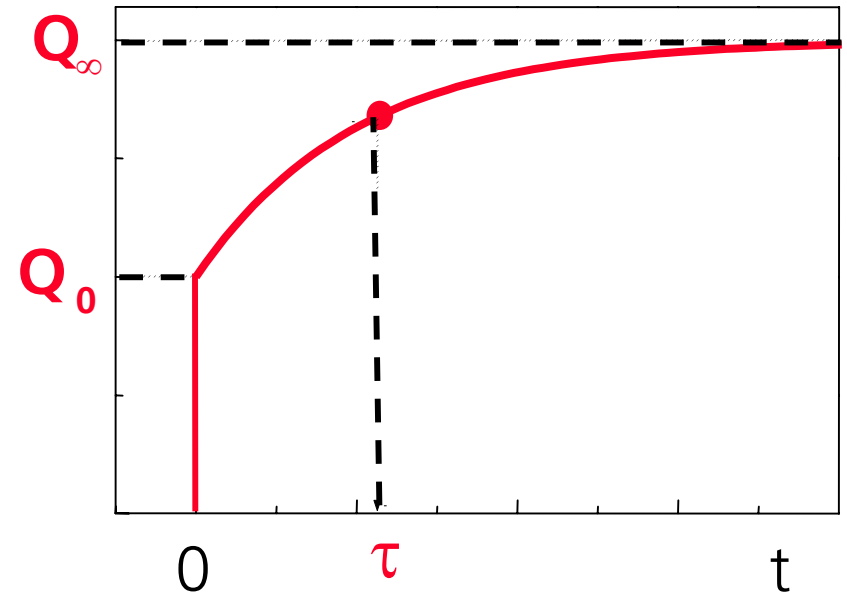
# Capacitive Resistivity Evaluation (III)

## Equivalent Circuit



$$\tau = R_s (C_s + C_a)$$

## Charge Transient after Voltage Step Application



$$\rho = Q_0 \tau (Q_\infty \epsilon \epsilon_0)^{-1}$$

# IAF - COREMA - 2000

## Specifications

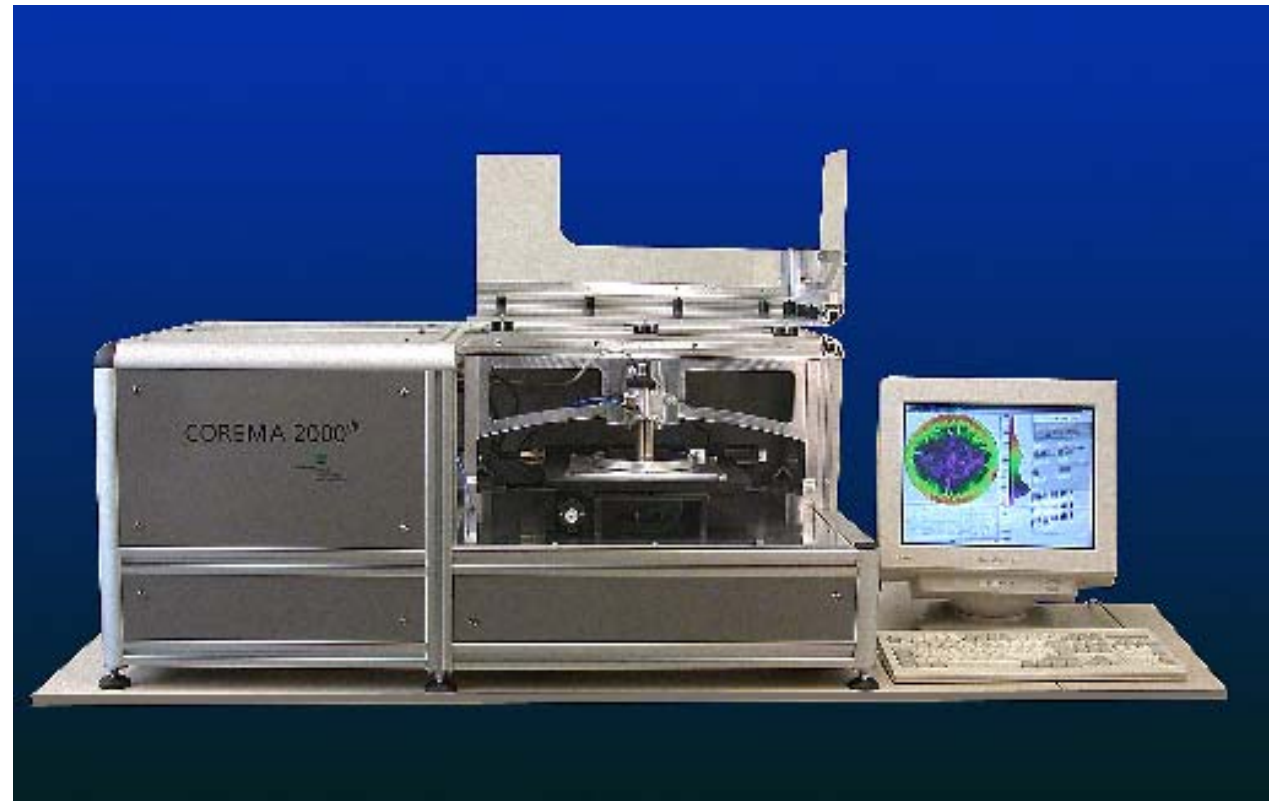
Range  $1 \times 10^5 - 1 \times 10^{12} \Omega \text{cm}$

Wafer  $\varnothing \leq 200 \text{ mm}$

Probe  $\varnothing \leq 1 \text{ mm}$

Repeatability 1%

Speed 100 ms @  $1 \text{E}7 \Omega \text{cm}$





# SiC SUBSTRATE RESISTIVITY TOPOGRAPHY (I)

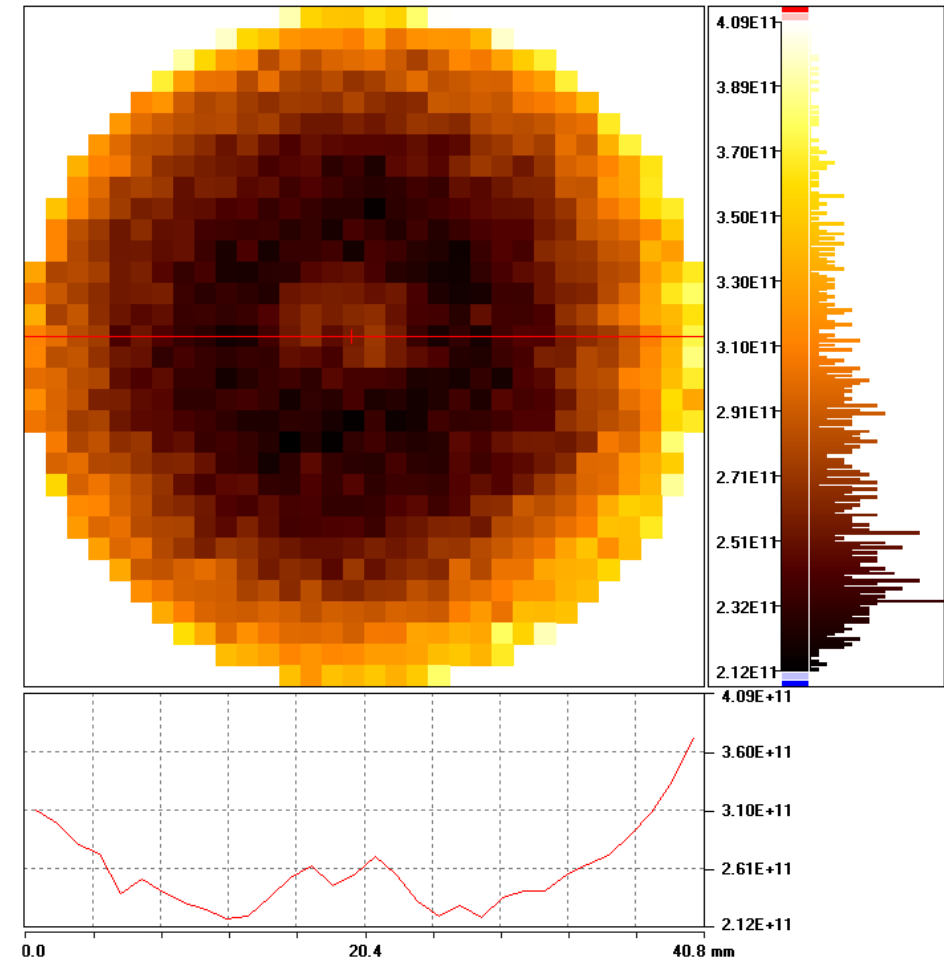
2" Wafer

$\rho$ :  $2.12 \times 10^{11} - 4.09 \times 10^{11} \Omega\text{cm}$

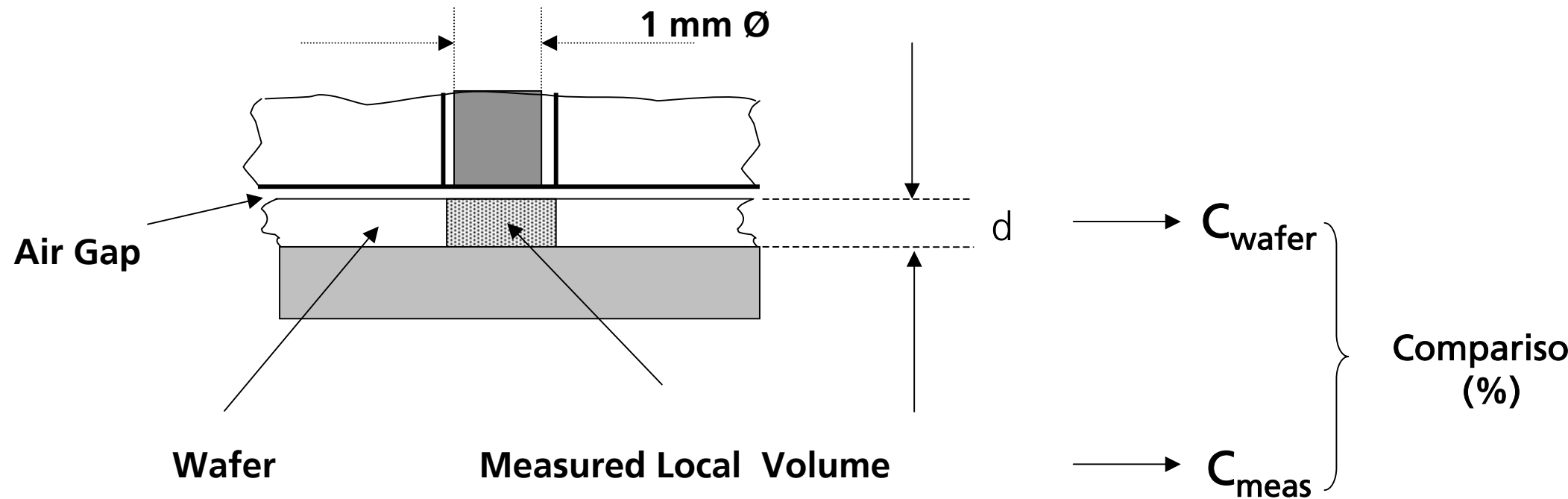
Mean:  $2.75 \times 10^{11} \Omega\text{cm}$

Stdv: 14.7%

Very homogeneous Material



# Evaluation of local Homogeneity (I)



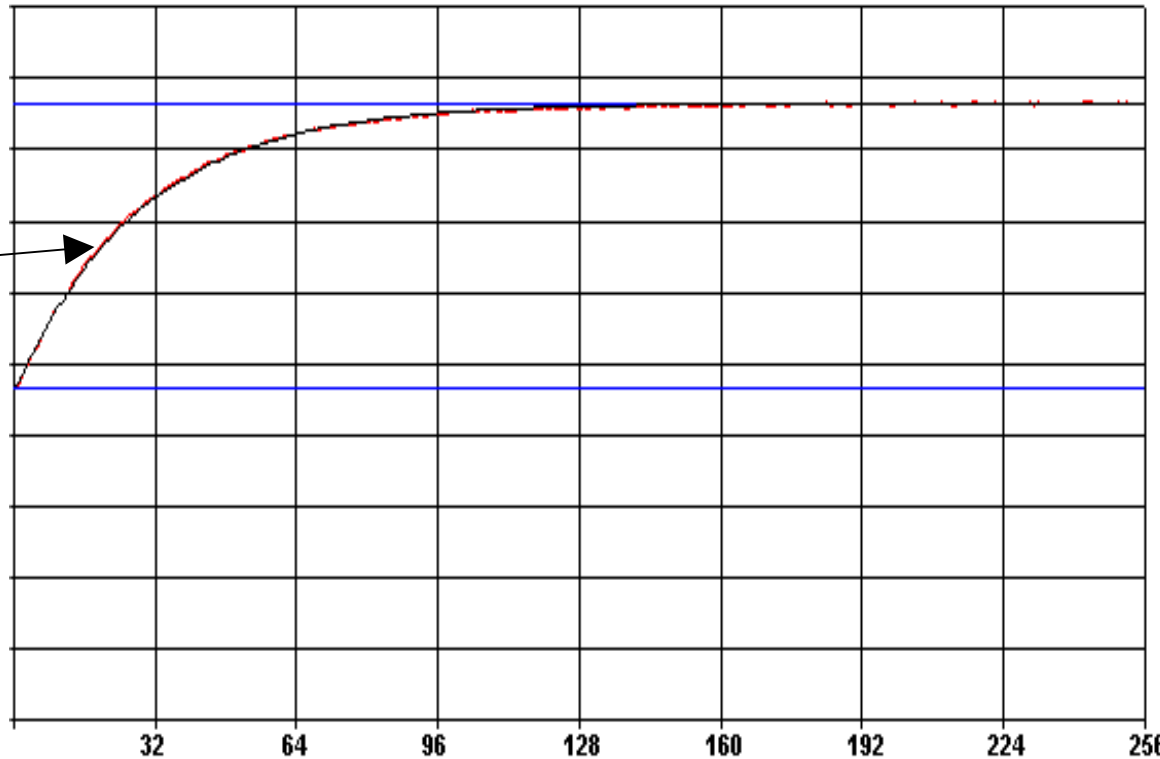
# Evaluation of local Homogeneity (II)

Measured values

Fit

$Q_\infty$

$Q_0$



Theoretical values

$Q_\infty^*$

Expected Transient

$Q_0^*$

$$Q_0 = Q_0^*$$

Volume of resistive Material = 100% (homogeneous)

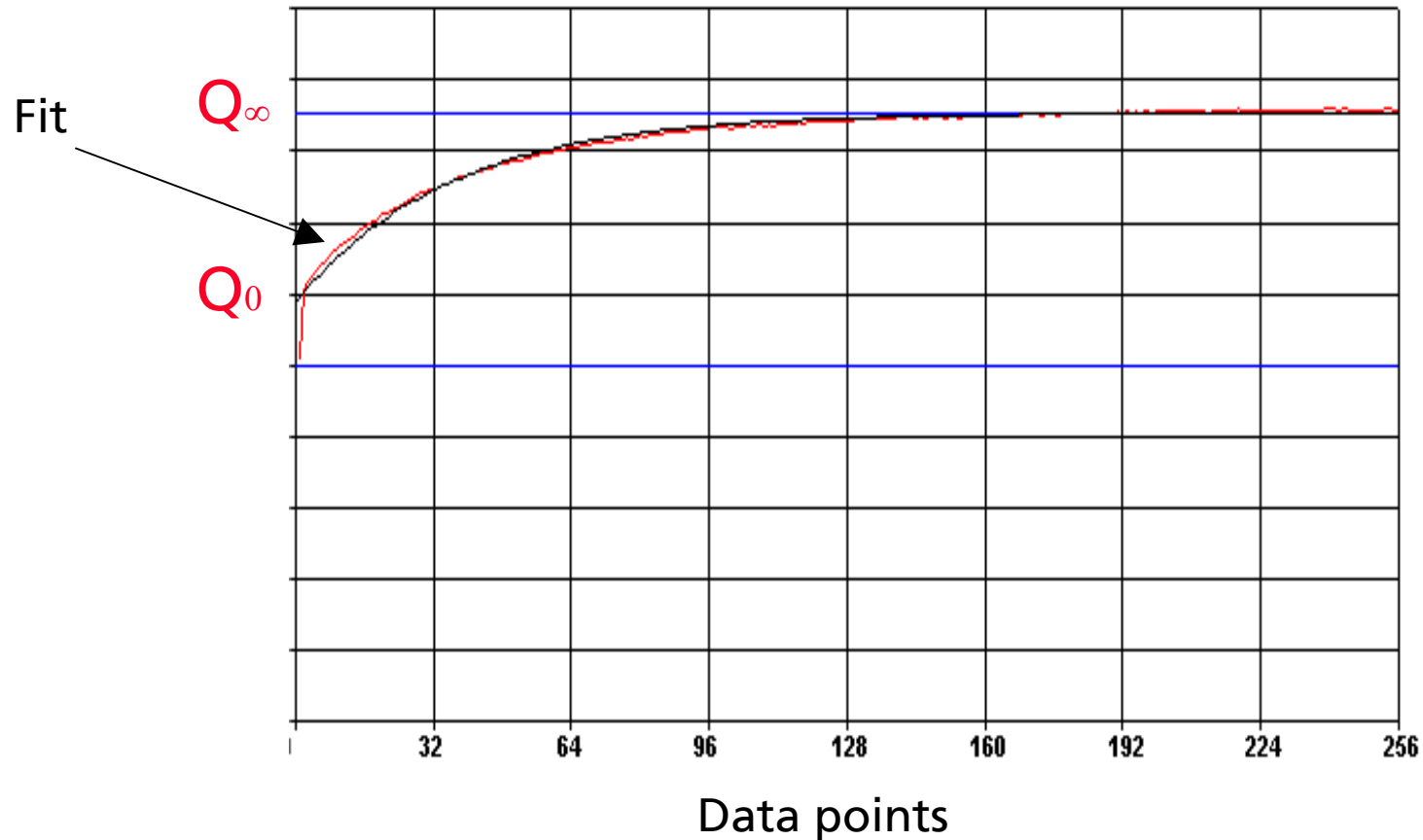
Volume Topogram



# Evaluation of local Homogeneity (III)

Measured values

Theoretical values



$Q_\infty^*$

Expected Transient

$Q_0^*$

$$Q_0 > Q_0^*$$

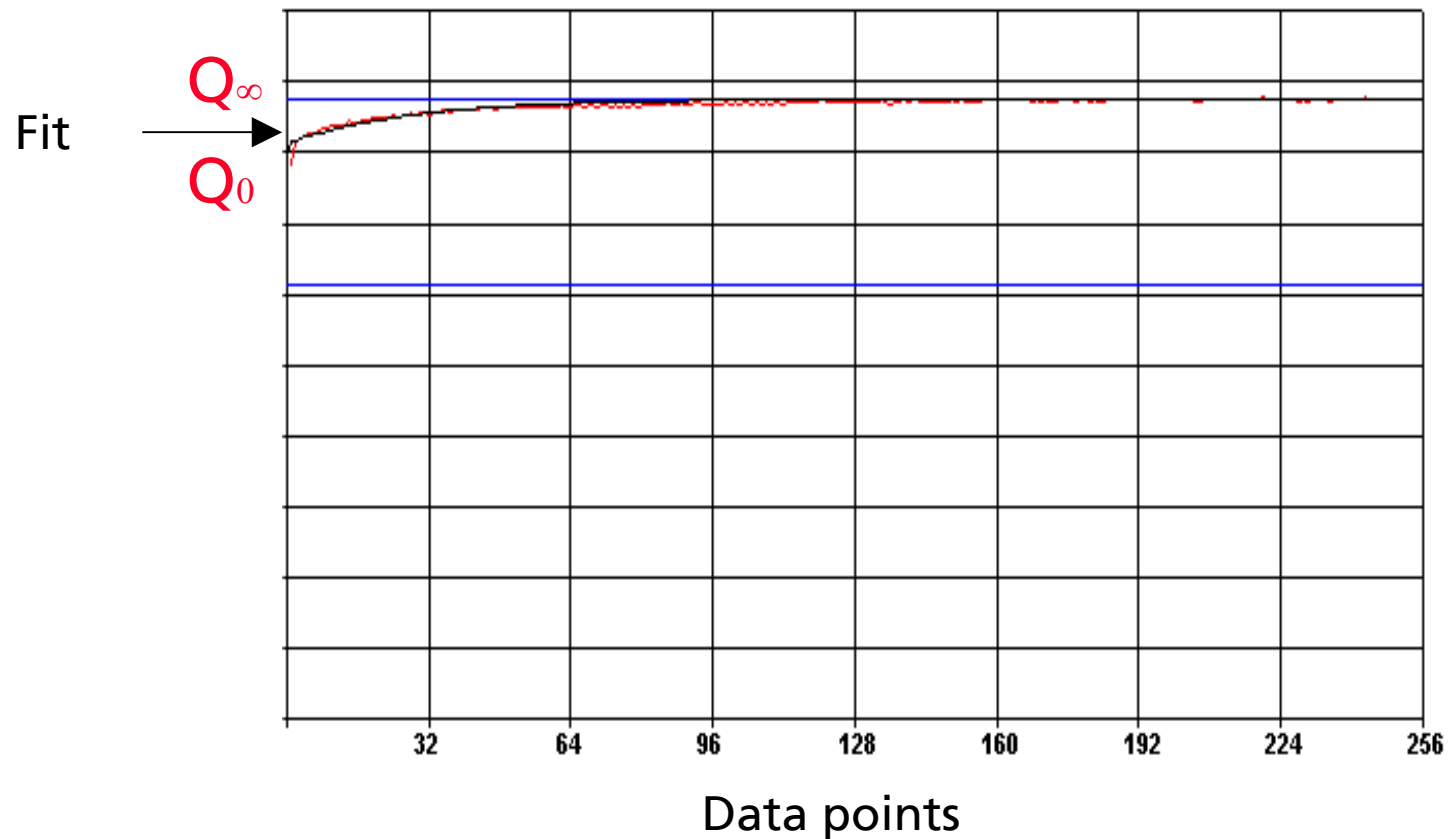
Volume of resistive Material < 100% (inhomogeneous)



# Evaluation of local Homogeneity (IV)

Measured values

Theoretical values



$Q_\infty^*$

$Q_0^*$

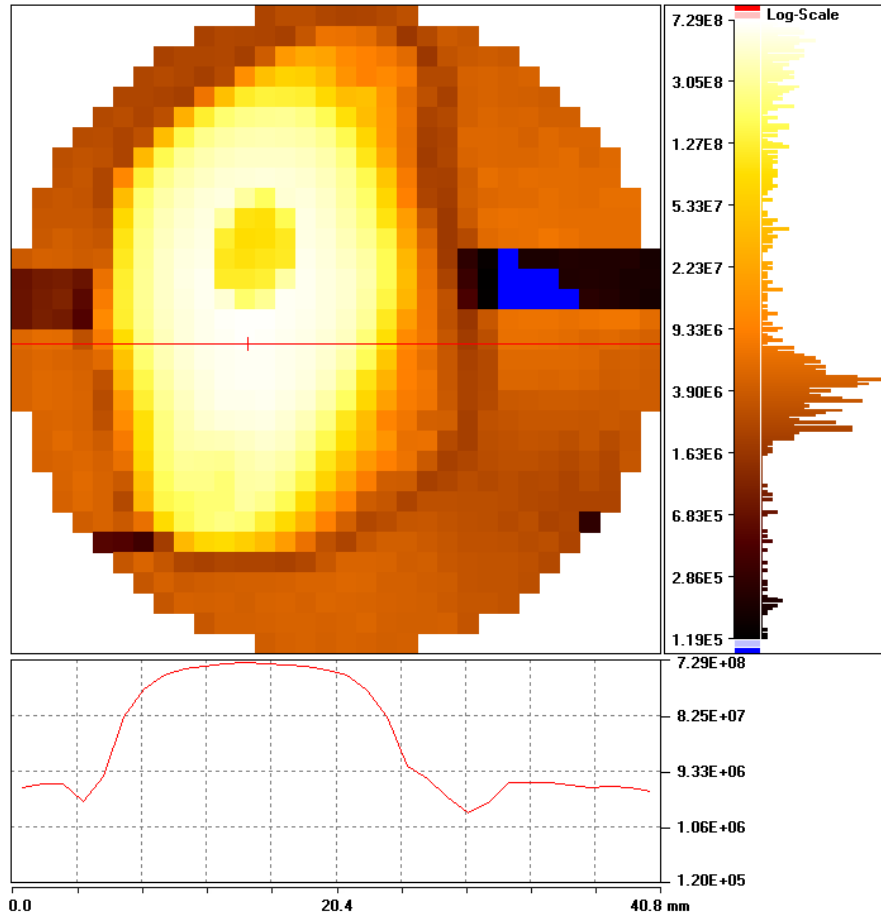
Expected Transient

$$Q_0 \gg Q_0^*$$

Volume of resistive Material  $\ll 100\%$  (inhomogeneous)

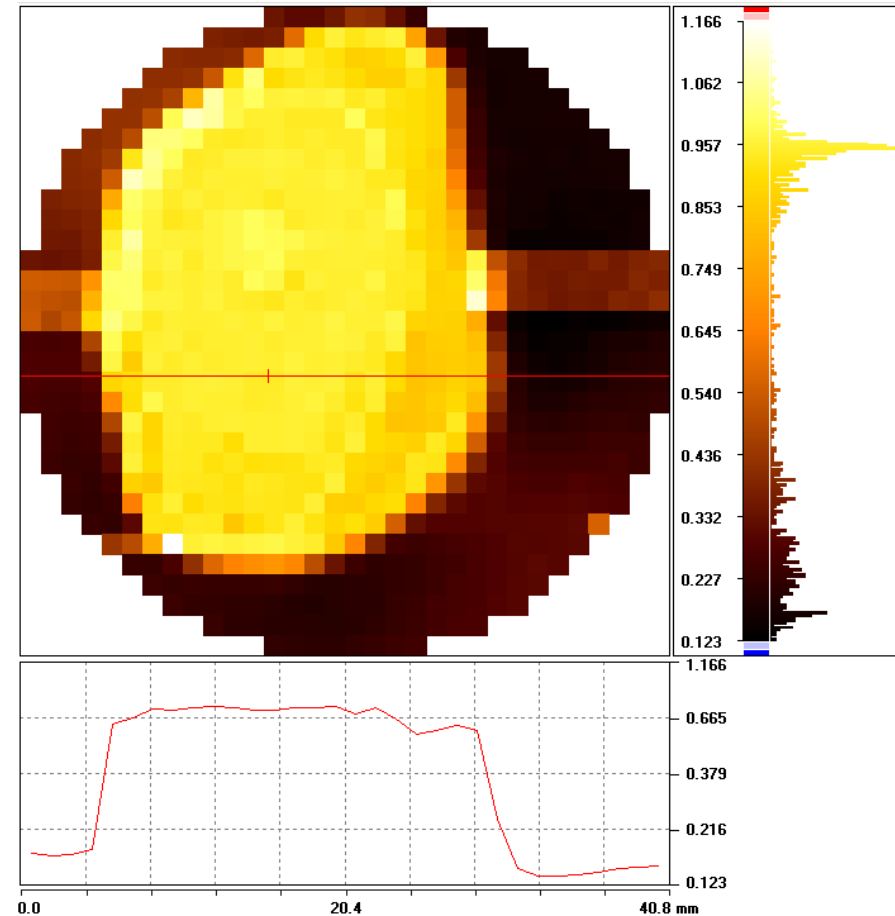
# SiC SUBSTRATE RESISTIVITY TOPOGRAPHY (II)

## Resistivity



Mean:  $1.13 \times 10^8 \Omega\text{cm}$   
Stdv: 168 %

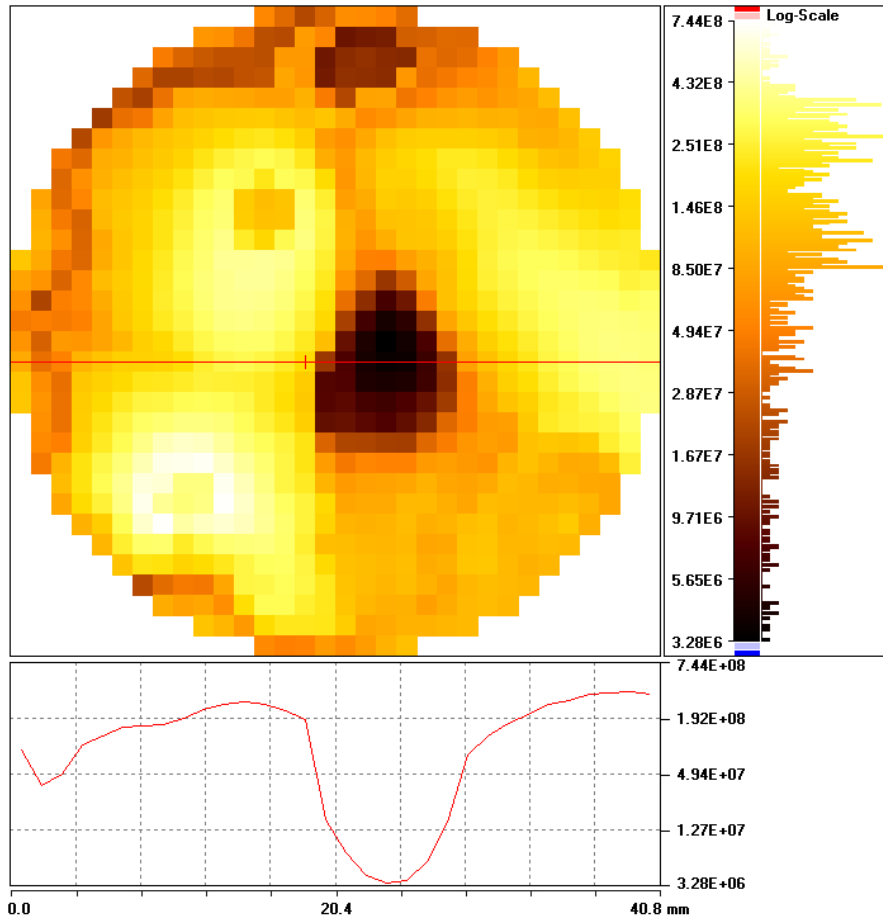
## Volume



Mean: 62%  
Stdv: 54%

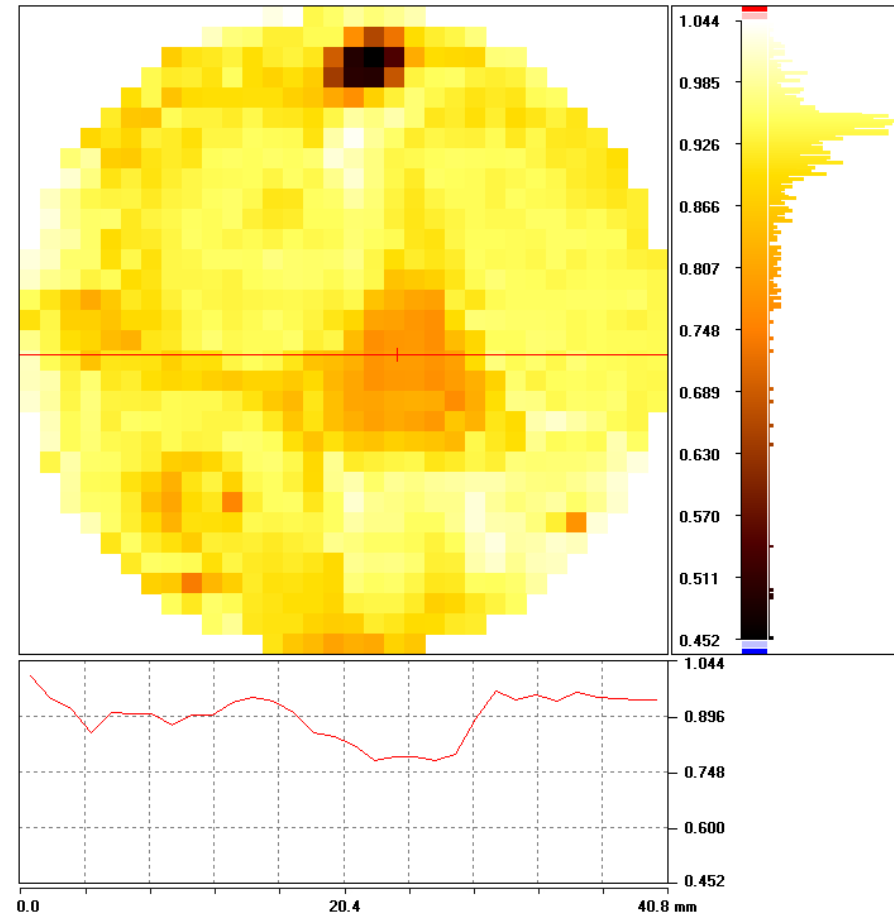
# SiC SUBSTRATE RESISTIVITY TOPOGRAPHY (III)

## Resistivity



Mean:  $1.72 \times 10^8 \Omega\text{cm}$   
Stdv: 75 %

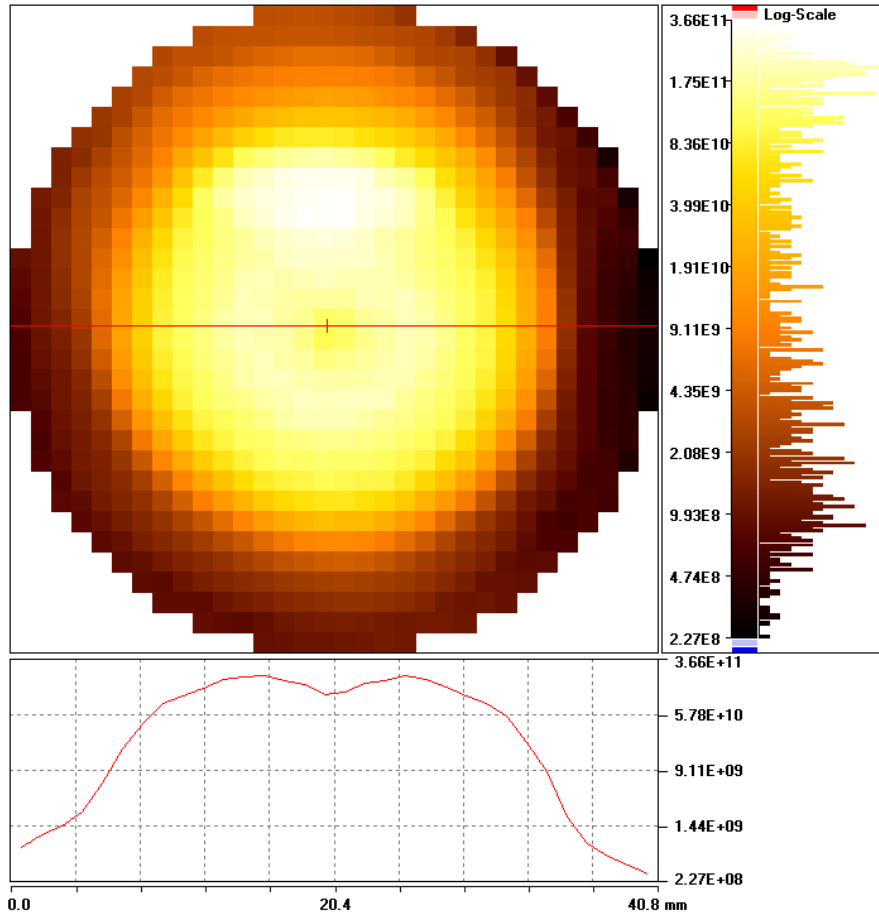
## Volume



Mean: 92.4%  
Stdv: 6.76%

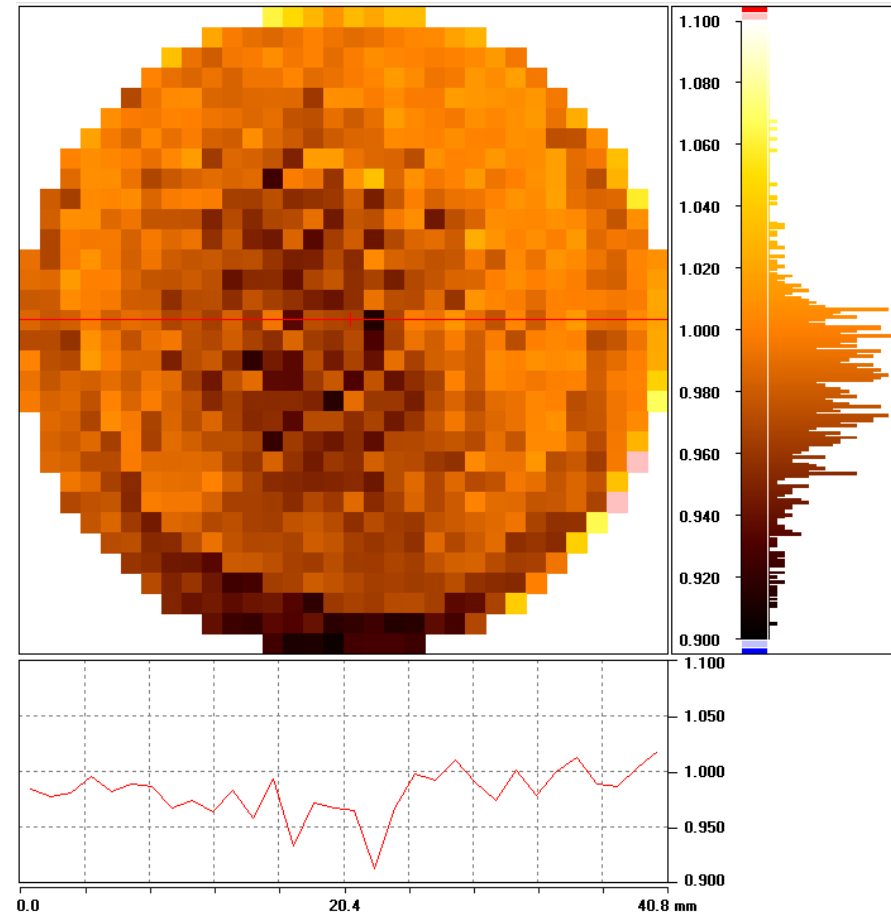
# SiC SUBSTRATE RESISTIVITY TOPOGRAPHY (IV)

## Resistivity



Mean:  $5.82 \times 10^{10} \Omega\text{cm}$   
Stdv: 143 %

## Volume

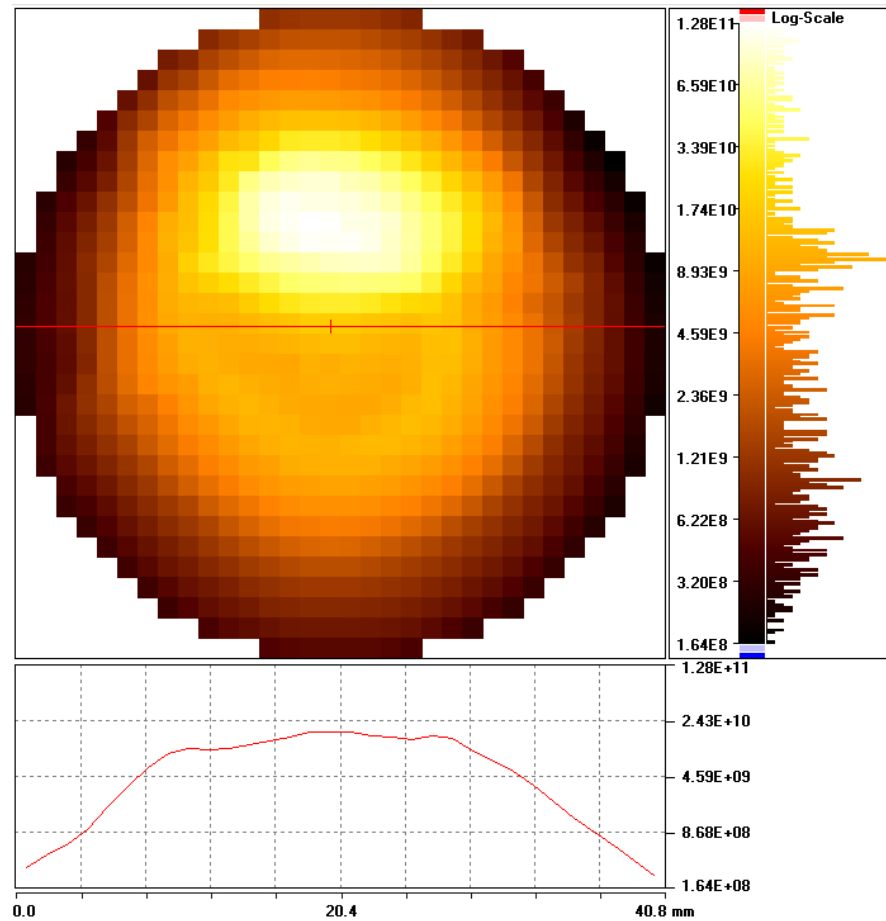


Mean: 98.2%  
Stdv: 2.54%



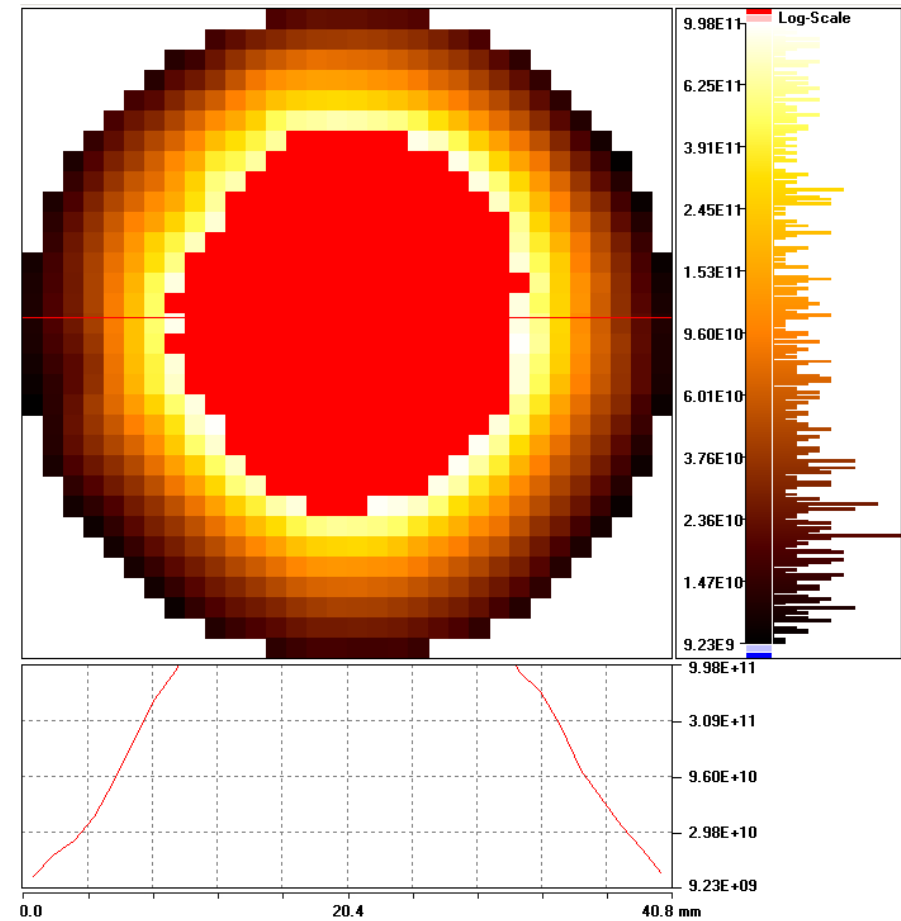
# SiC SUBSTRATE RESISTIVITY TOPOGRAPHY (V)

## Resistivity (3 h darkness)



Mean:  $1.12 \times 10^{10} \Omega\text{cm}$

## Resistivity (>48 h darkness)



Mean:  $1.65 \times 10^{11} \Omega\text{cm}$

# Zusammenfassung

- Kapazitive Widerstandsmessung im Bereich  $1 \times 10^5$  bis  $1 \times 10^{12} \Omega \text{cm}$
- Topografie semi-isolierender SiC Substrate
- Bestimmung des Volumen-Anteils bei lokal inhomogenen Material